

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8	((("5778439") or ("5760602") or ("5036473") or ("5109353") or ("5140526") or ("5430687") or ("5500609") or ("5504440")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:20
L2	248	712/10.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L3	196	712/37.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L4	754	712/227.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L5	113	FPGA and ("shift register" same circular)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L6	1	FPGA and ("shift register" same circular same clb)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L7	59	L5 and selector	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L8	188	("4642487" "4700187" "4703435" "4706216" "4722084" "4724307" "4744084" "4758985" "4768196" "4786904" "4815003" "4829202" "4849928" "4868419" "4908772" "4935734" "4949275").PN. OR ("5036473").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/01 10:21

L9	67	L8 and "shift register"	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/01 10:21
L10	1388	(326/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:21
L11	782	L10 and fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L12	1	tremblay.in. and fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L13	89	tremblay.in. and programmable	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L14	35	(fpga or "programmable logic") and ((cache or cached or time-multiplexed or paged or overlay) same CLB)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L15	136	"time-multiplexed" same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L16	6	paged same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L17	26	"dynamically reconfigurable fpga"	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L18	1	("5,646,545").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:21
L19	37	overlay with fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L20	354	partition\$2 with fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L21	54	partition\$2 with clb	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L22	9	selecting near2 CLB	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L23	20	L21 and selector	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L24	32	L21 and (CLB same multiplex\$2)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L25	54	partition\$2 with clb	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21

L26	20	L25 and selector	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L27	248	712/10.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L28	113	FPGA and ("shift register" same circular)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L29	196	712/37.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L30	32	L25 and (CLB same multiplex\$2)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L31	188	("4642487" "4700187" "4703435" "4706216" "4722084" "4724307" "4744084" "4758985" "4768196" "4786904" "4815003" "4829202" "4849928" "4868419" "4908772" "4935734" "4949275").PN. OR ("5036473").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/01 10:21
L32	136	"time-multiplexed" same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L33	67	L31 and "shift register"	US-PGPUB; USPAT; USOCR	AND	ON	2008/04/01 10:21
L34	26	"dynamically reconfigurable fpga"	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L35	354	partition\$2 with fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L36	1388	(326/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:21
L37	1	tremblay.in. and fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21

L38	59	L28 and selector	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L39	89	tremblay.in. and programmable	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L40	35	(fpga or "programmable logic") and ((cache or cached or time- multiplexed or paged or overlay) same CLB)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L41	8	((("5778439") or ("5760602") or ("5036473") or ("5109353") or ("5140526") or ("5430687") or ("5500609") or ("5504440")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:21
L42	1	("5,646,545").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/04/01 10:21
L43	37	overlay with fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L44	782	L36 and fpga	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L45	6	paged same (fpga or "programmable logic")	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L46	9	selecting near2 CLB	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L47	1	FPGA and ("shift register" same circular same clb)	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21
L48	754	712/227.ccls.	US-PGPUB; USPAT	AND	ON	2008/04/01 10:21

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